

FIG. 1

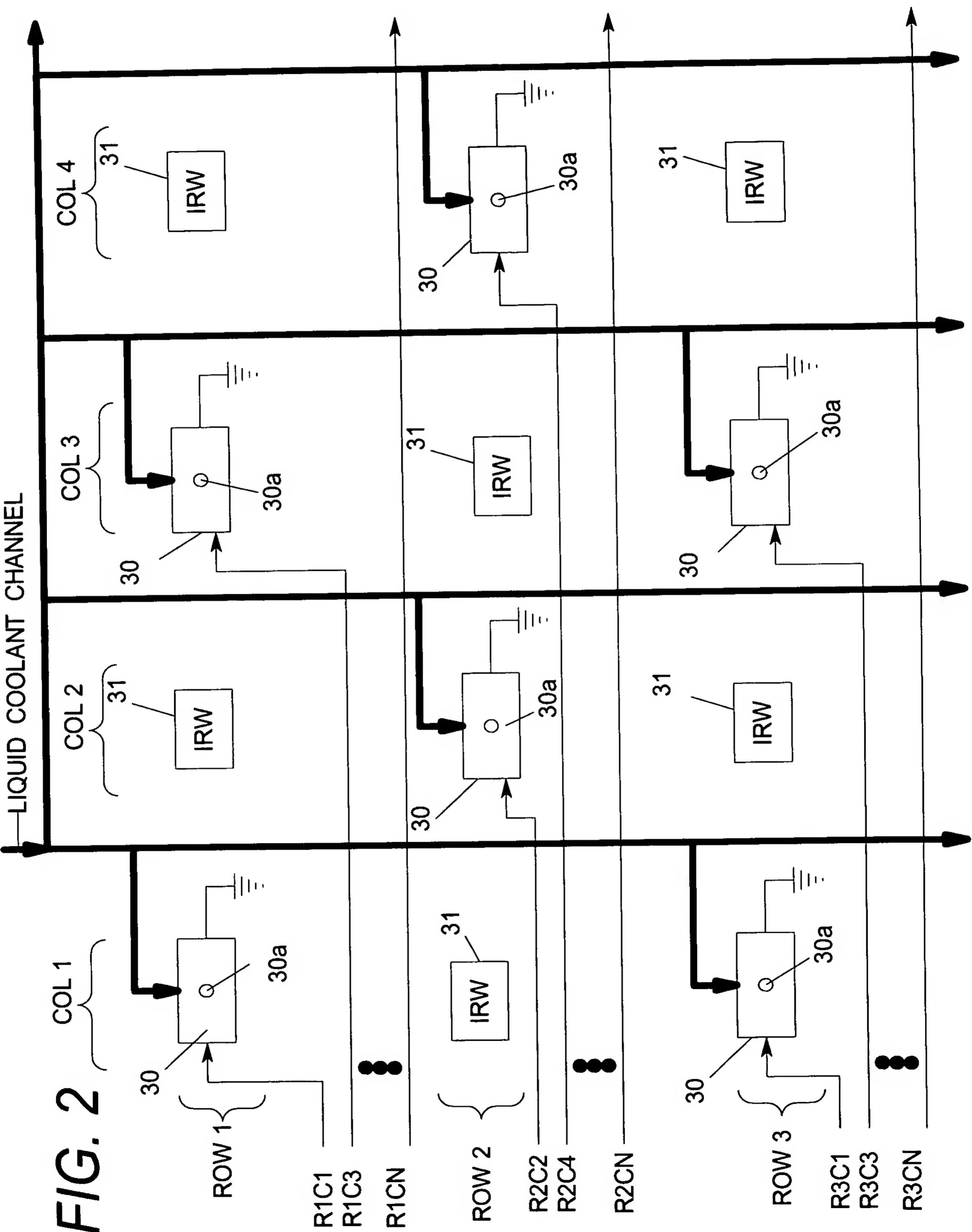
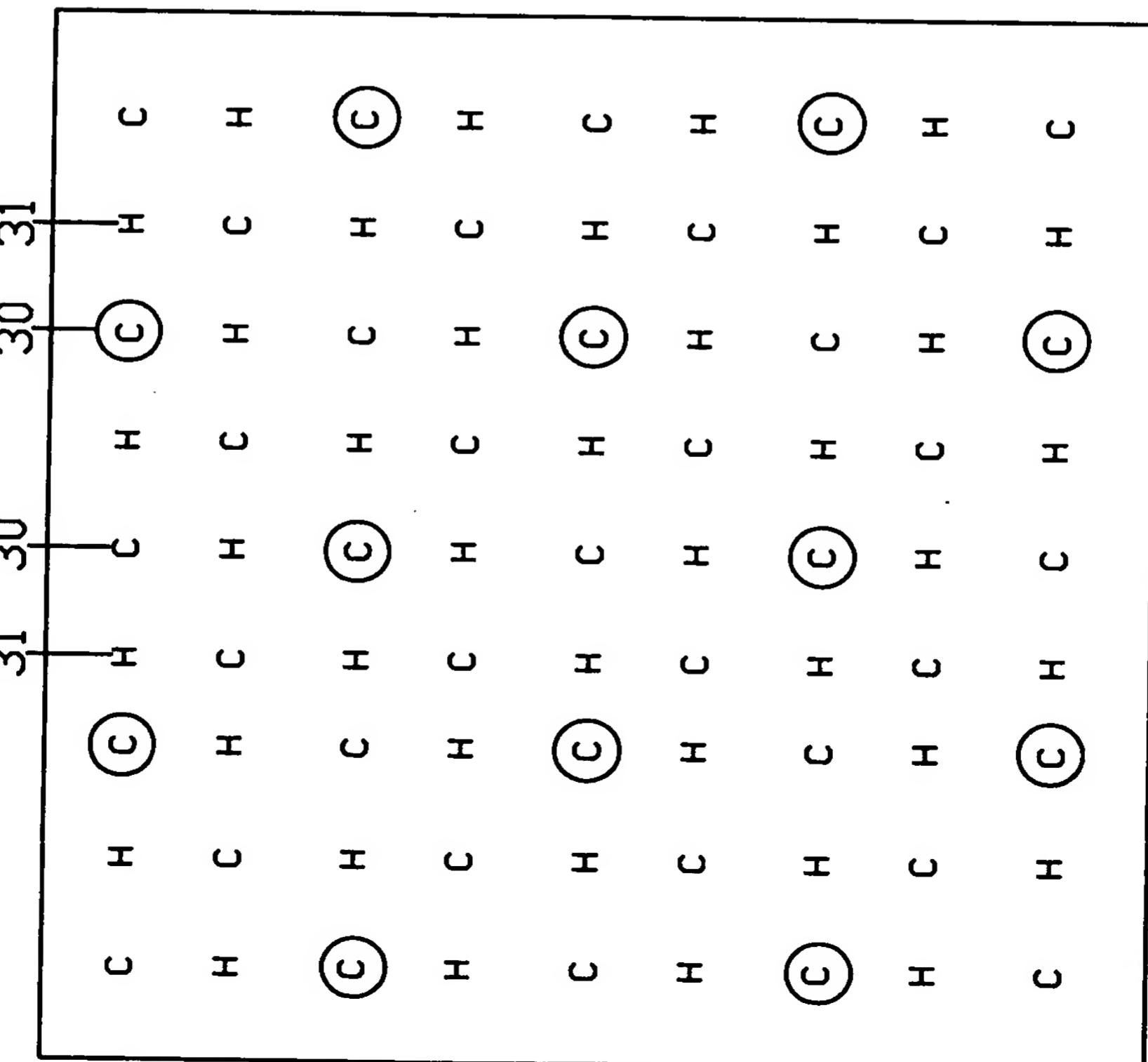
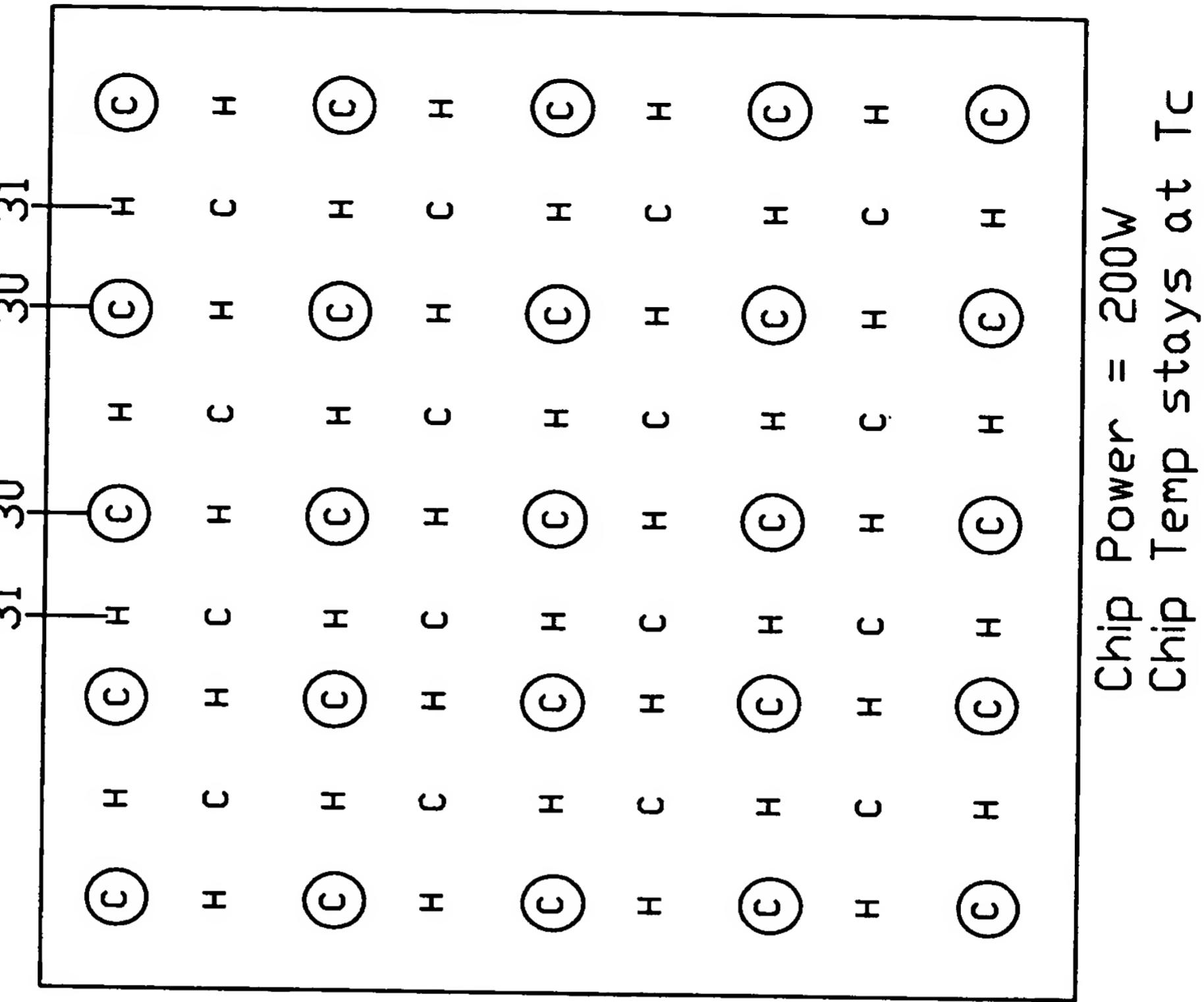


Fig 3A



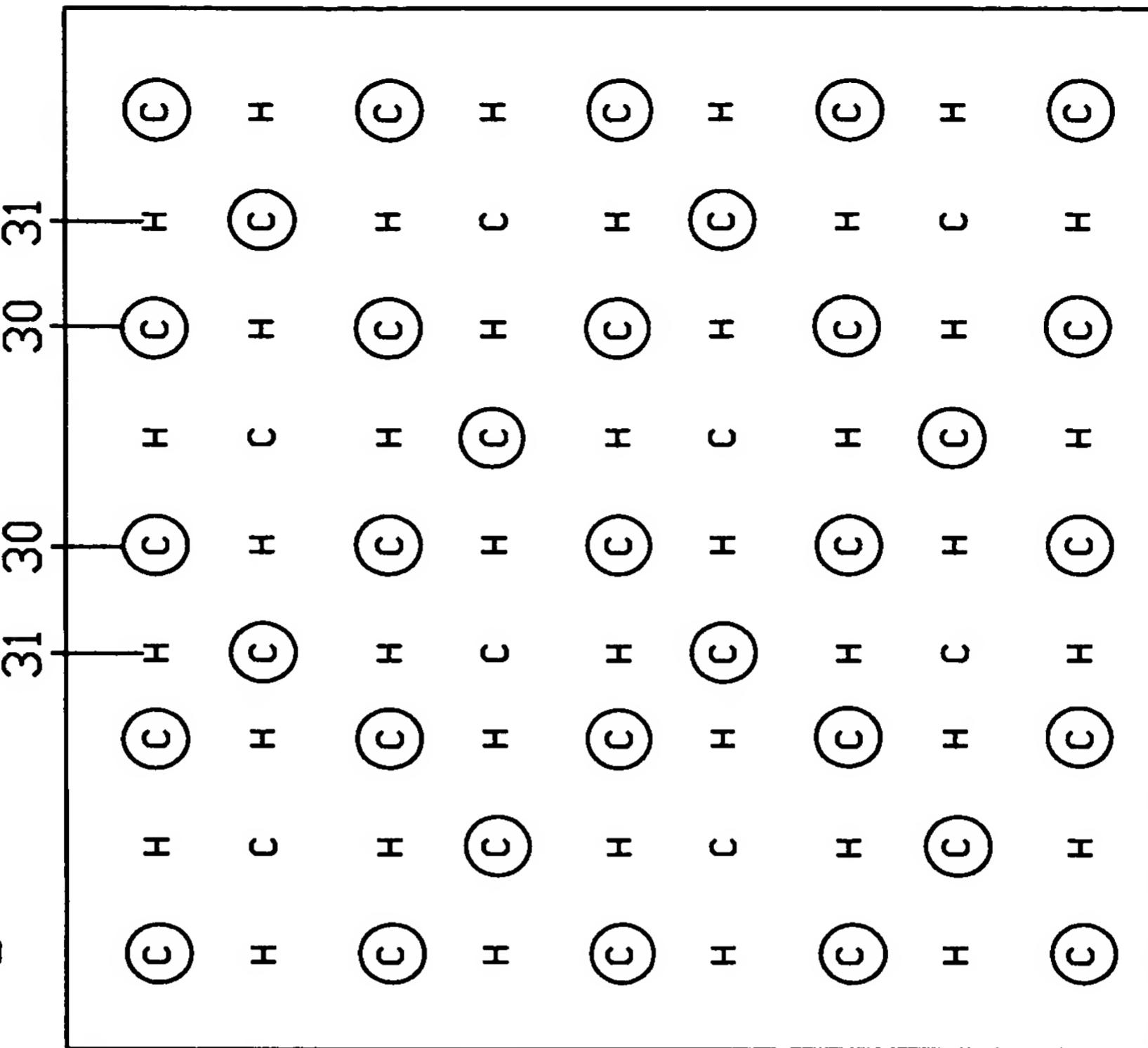
$$\begin{aligned}\text{Chip power} &= 100\text{W} \\ \text{Chip Temp} &= T_C\end{aligned}$$

3B
3
E



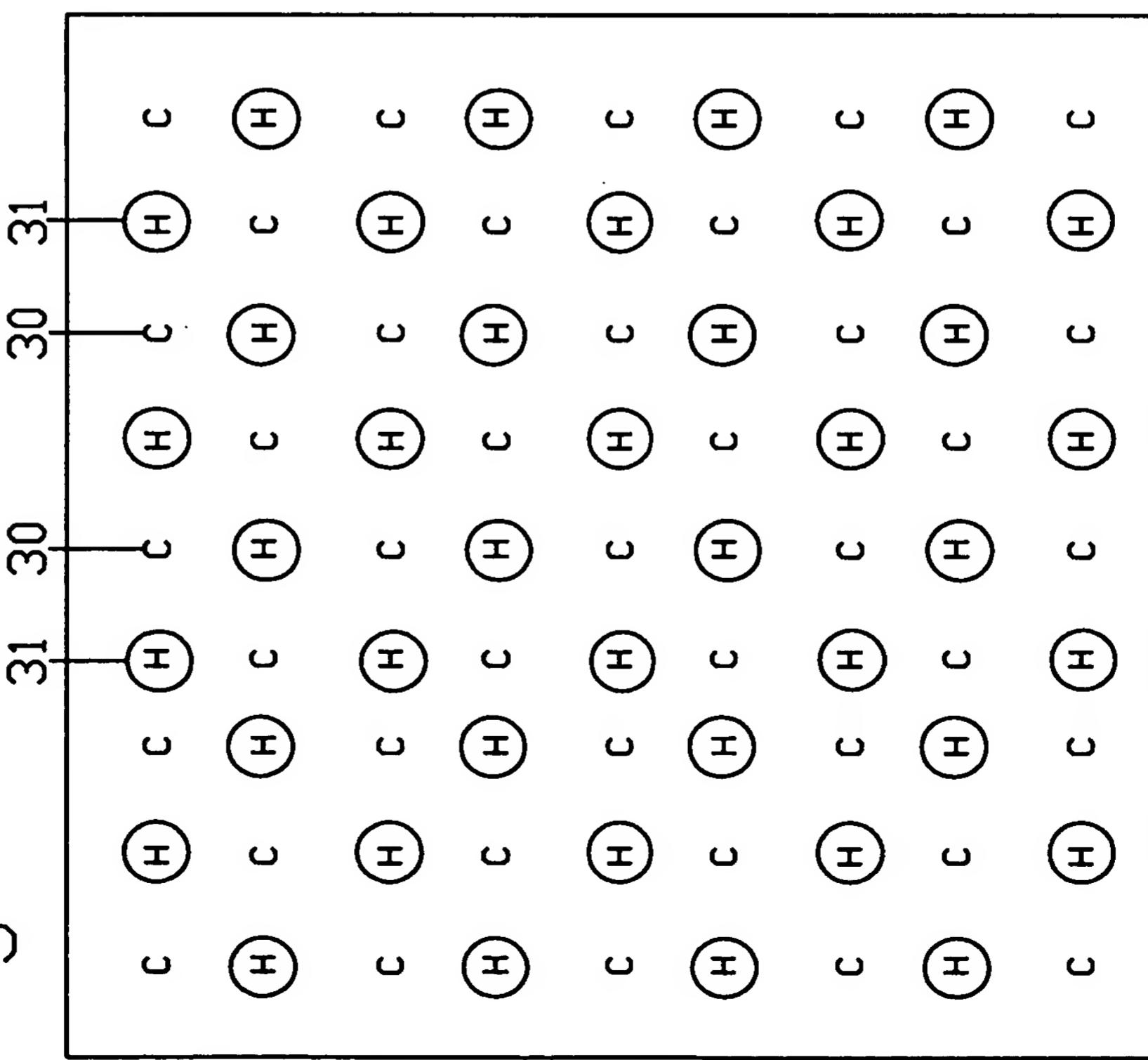
Chip Power = 200W
Chip Temp stays at Tc

AMERICAN



Chip Power = 300W
Chip Temp stays at T_C

三
上



Chip Power = 0W
Chip Temp stays at Tc

FIG. 4

$$eq. 1 \sim 1 \text{ drop} = 10 \text{ picoliter} = 10 * 10^{12} \text{ lit} \quad \frac{10^3 \text{ gr}}{\text{lit}} = 10^8 \text{ gr}$$

$$eq. 2 \sim \Delta Q/\text{drop} = \left[(\Delta T)(c_p) + 2260 \frac{J}{\text{gr}} \right] \frac{10^{-8} \text{ gr}}{\text{drop}} \approx 20 \frac{\mu J}{\text{drop}}$$

$$eq. 3 \sim 400 \frac{J}{\text{sec}} = 20 \frac{\mu J}{\text{drop}} \left[\begin{matrix} \# \text{ of} \\ \text{nozzles} \end{matrix} \right] \left[\begin{matrix} \text{control} \\ \text{signal freq} \end{matrix} \right]$$

$$eq. 4 \sim \text{if freq} = 10^4 \text{ cycles/sec, then} \left[\begin{matrix} \# \text{ of} \\ \text{nozzles} \end{matrix} \right] = 2000$$

eq. 5 ~ nozzle array = (45) x (45) nozzles on 1 square inch

$$eq. 6 \sim \text{nozzle spacing} = \frac{2.54 \text{ cm}}{45 \text{ nozzles}} = \frac{560 \mu \text{ m}}{\text{nozzle}}$$

$$eq. 7 \sim \text{area per nozzle} = 50 \mu \text{m} \times 100 \mu \text{m}$$

$\text{area per IR-window} = 20 \mu \text{m} \times 20 \mu \text{m}$

FIG. 5A

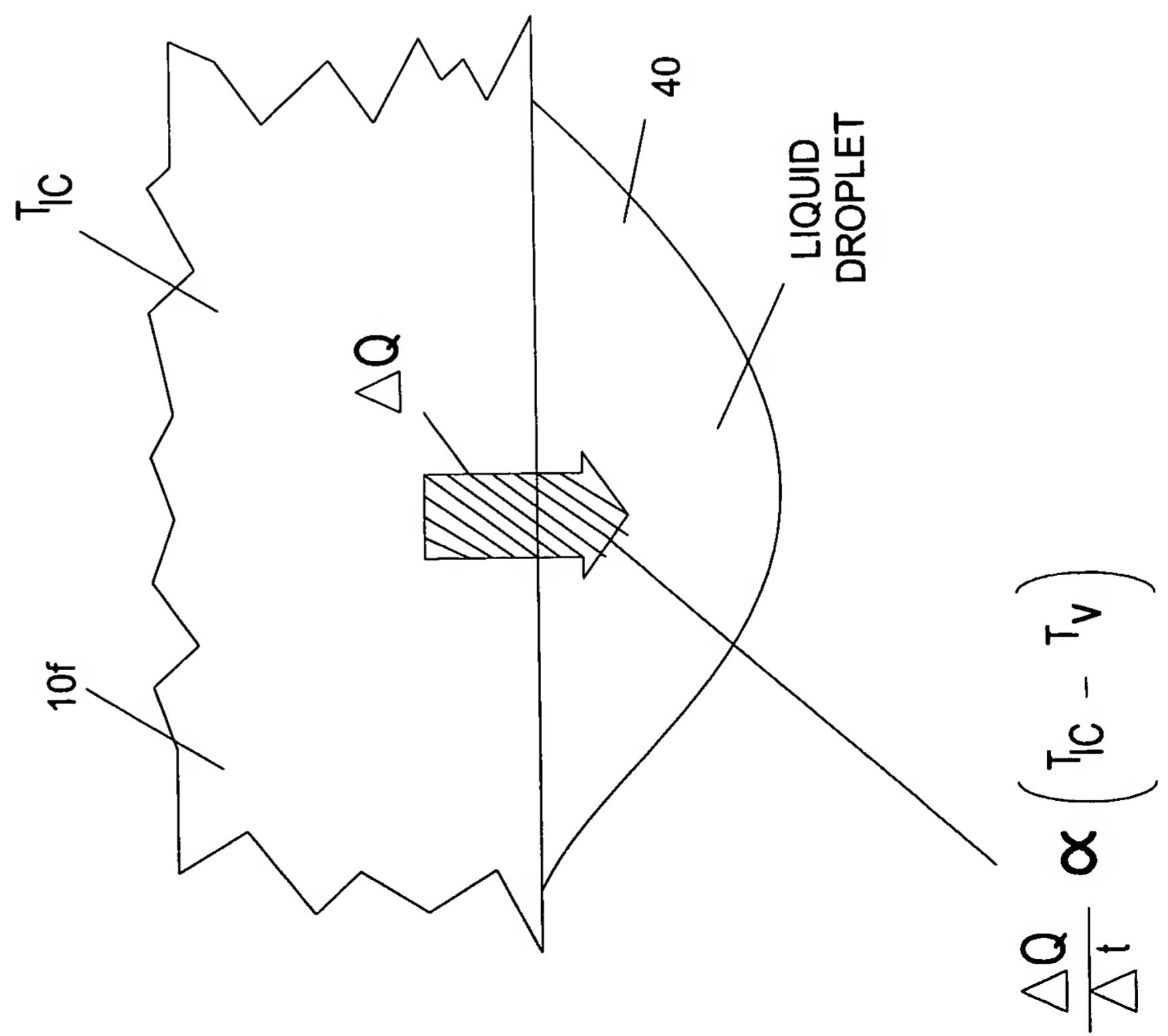


FIG. 5B

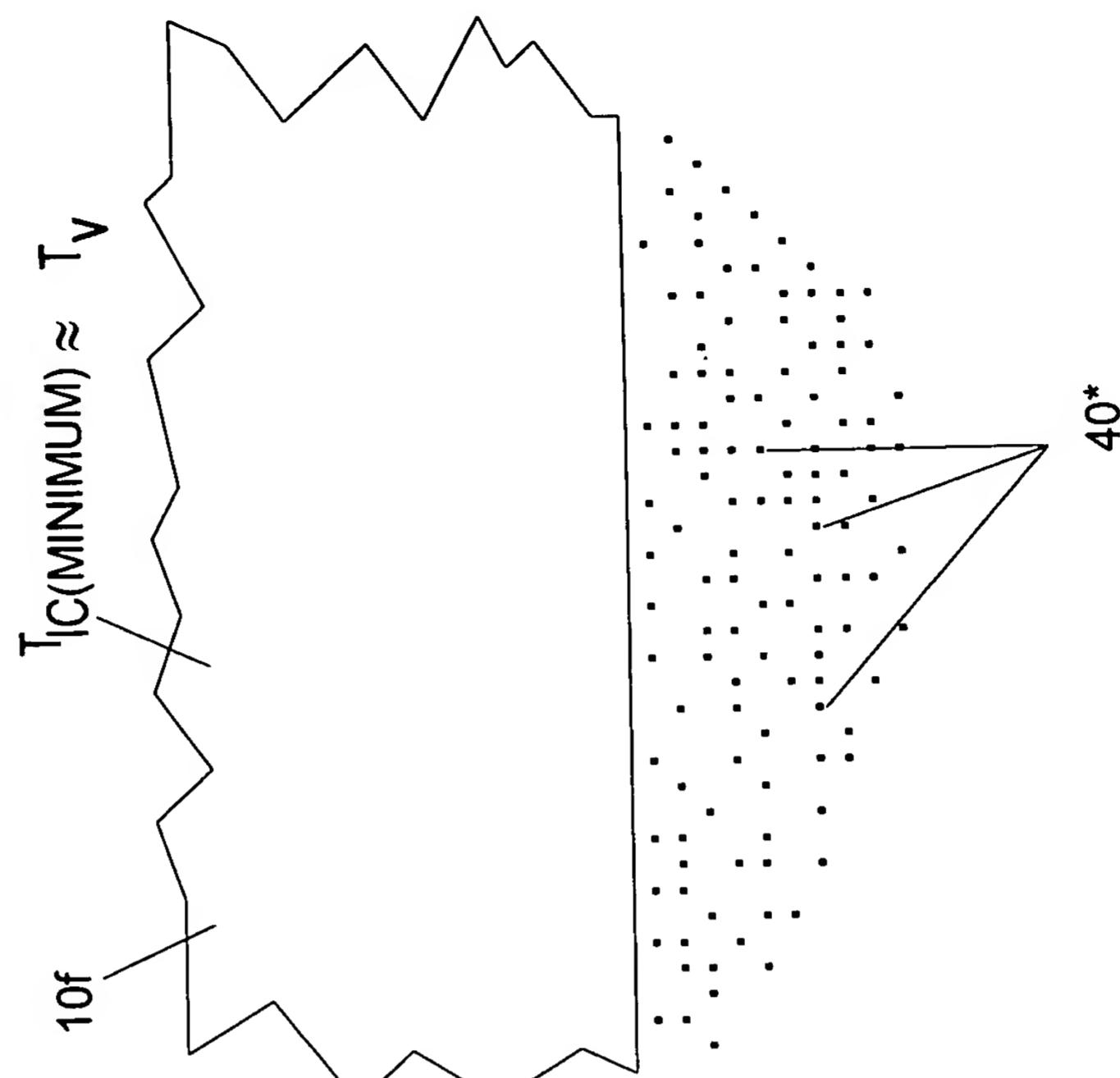


FIG. 6  COL 1 COL 2 COL 3

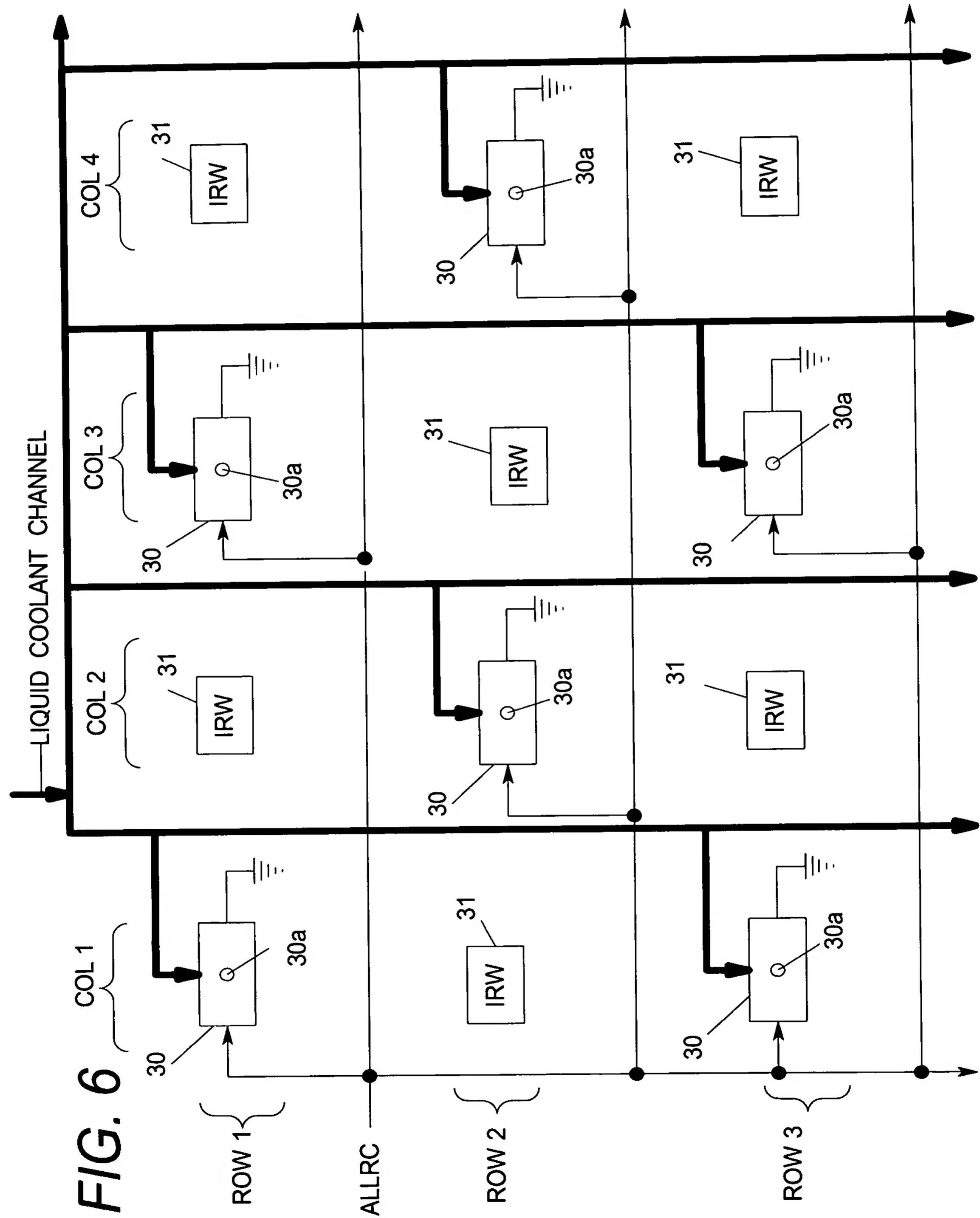
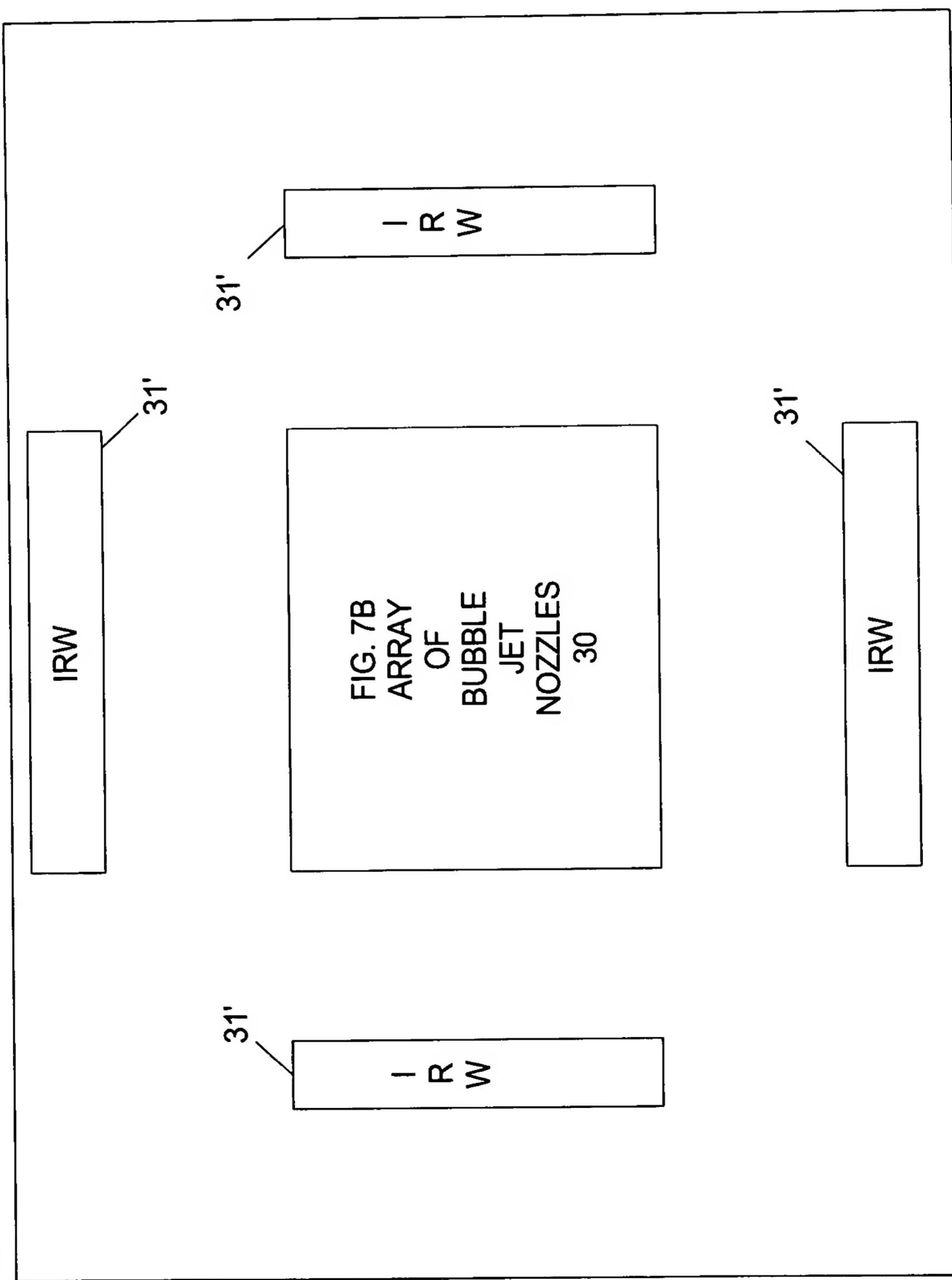


FIG. 7A



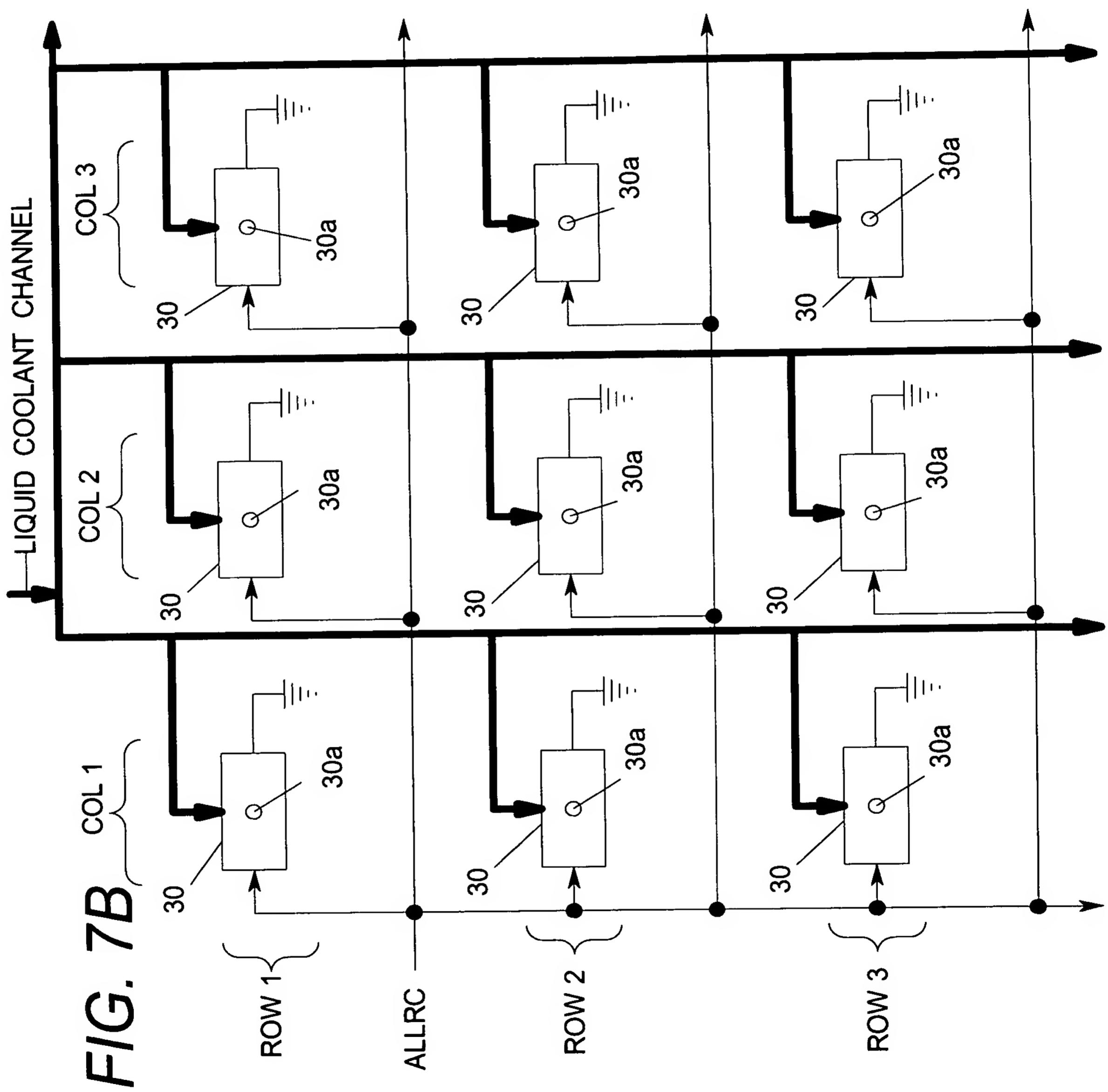


FIG. 7C

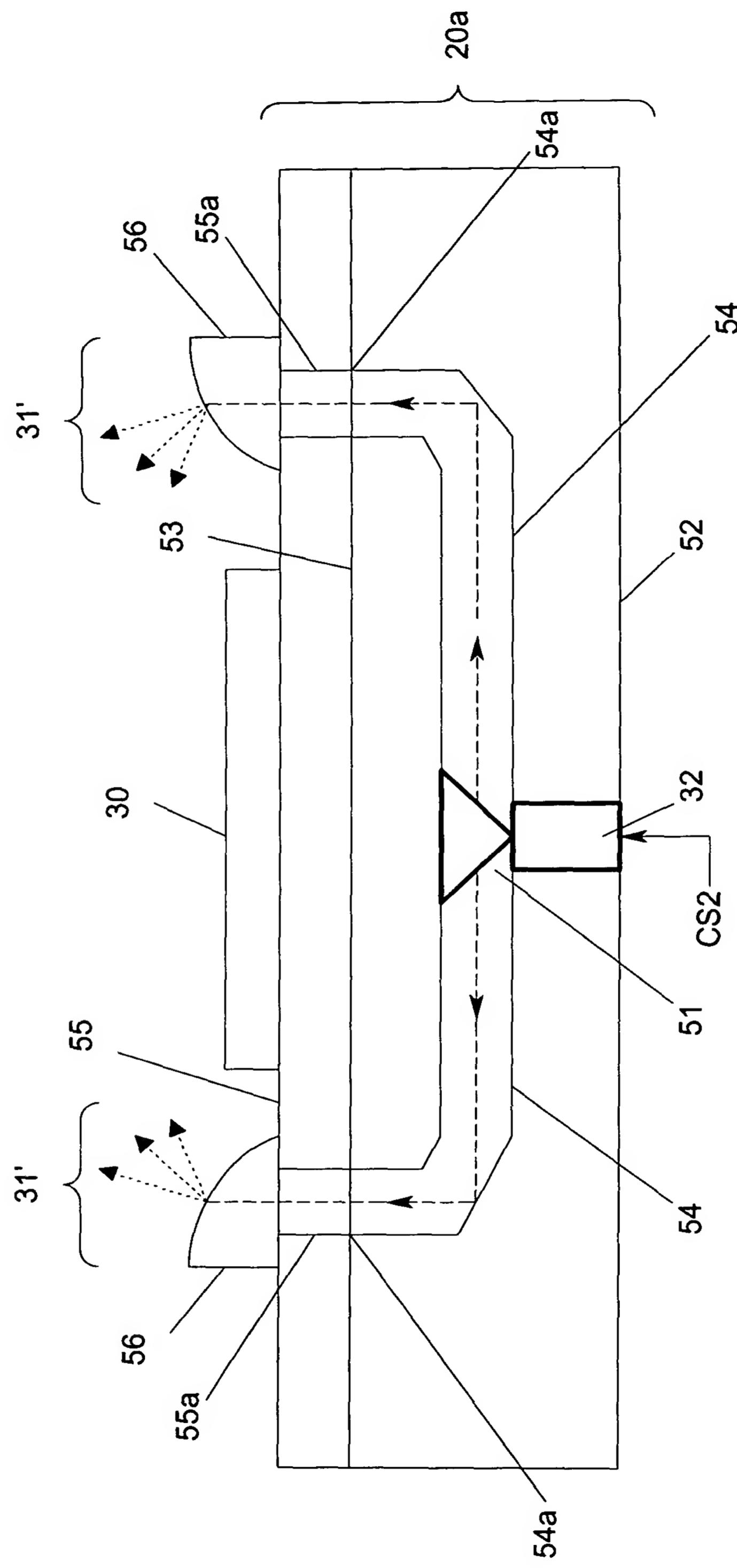


FIG. 8A

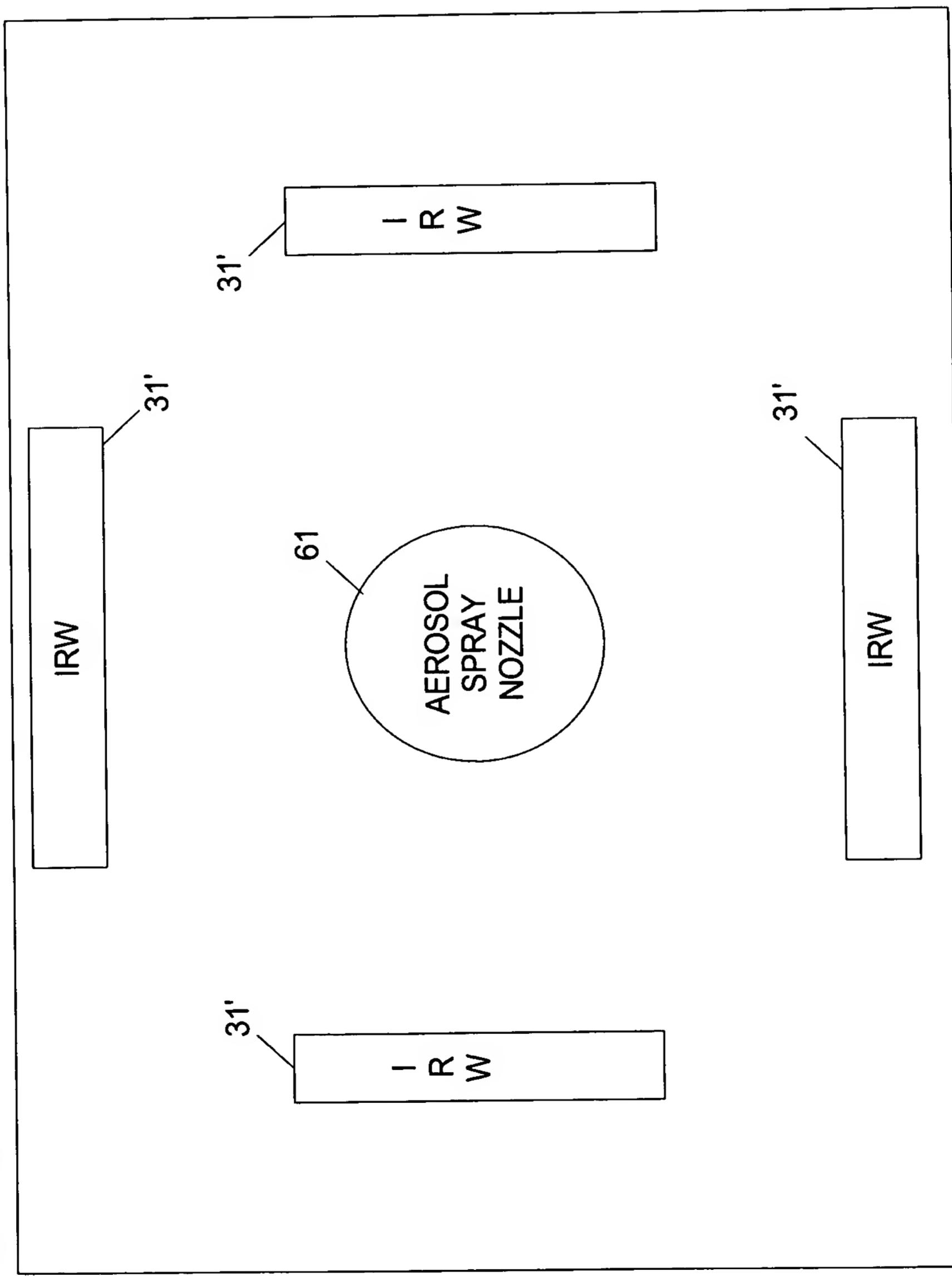
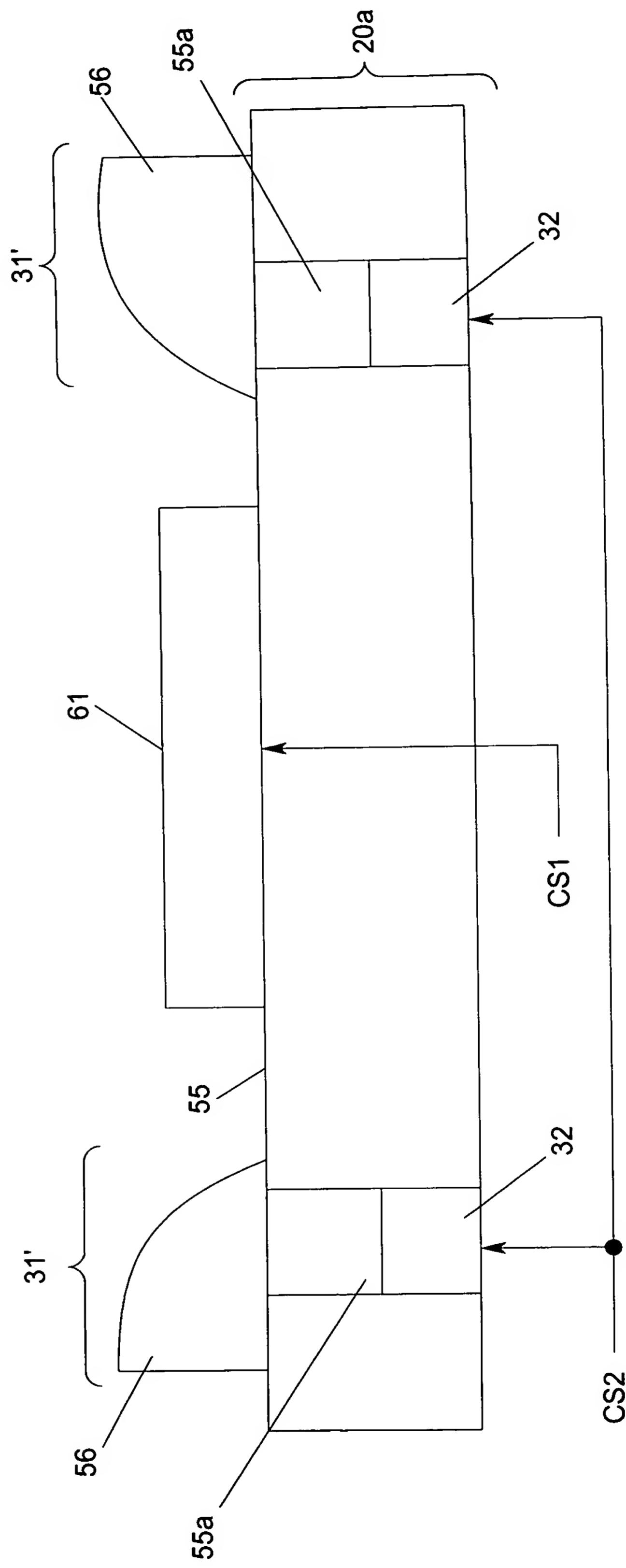


FIG. 8B



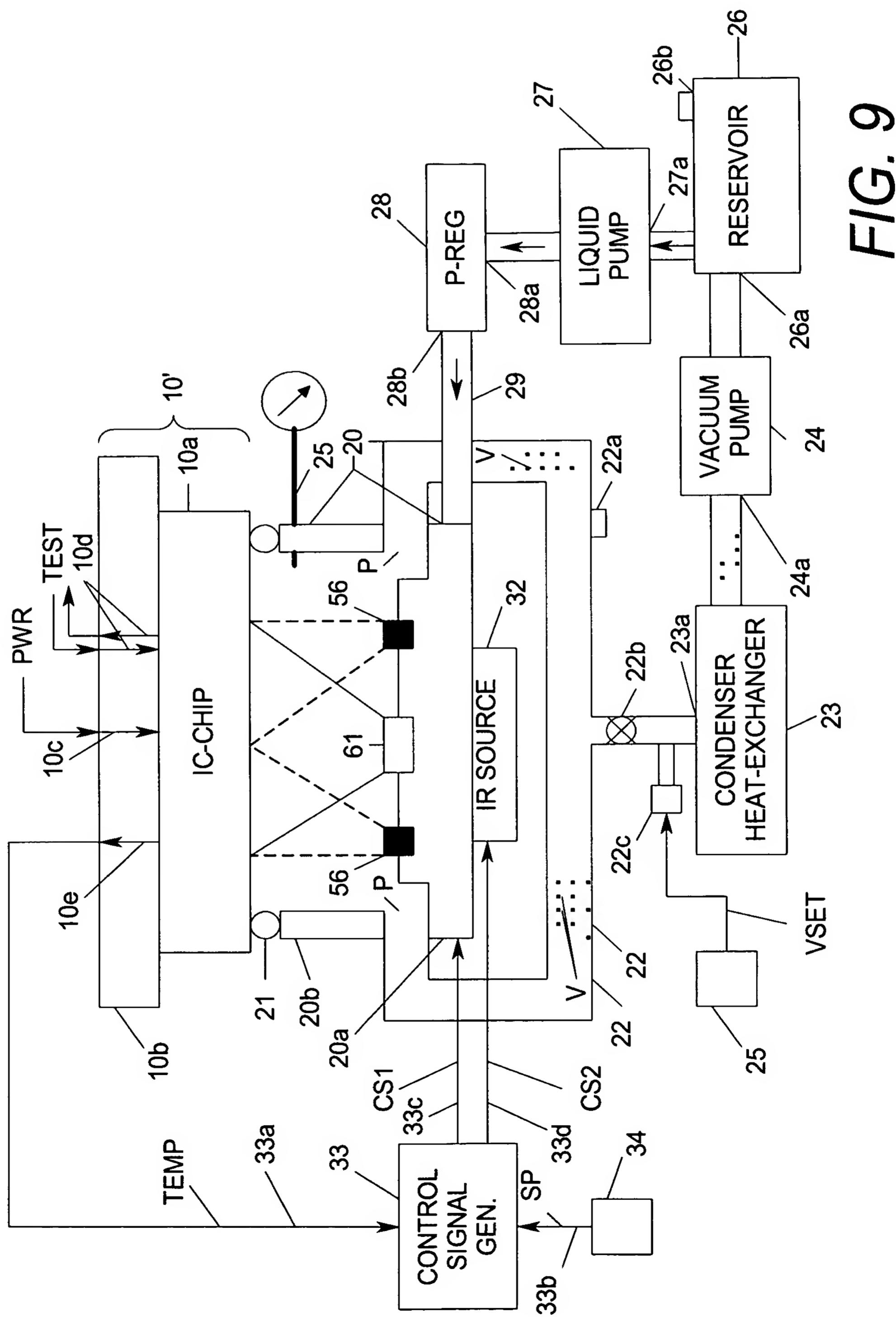


FIG. 9